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0756-864 28F

SERIAL NUMBER	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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08/051,313 04/23/93 TAKEMURA

Y 0756-864

EXAMINER
DUONG, T

E5M1/0110

SIXBEY, FRIEDMAN, LEEDOM & FERGUSON
2010 CORPORATE RIDGE, STE. 600
MCLEAN, VA 22102

ART UNIT PAPER NUMBER

2515

DATE MAILED: 01/10/95

Response Due: 4/10/95

This is a communication from the examiner in charge of your application.
COMMISSIONER OF PATENTS AND TRADEMARKS

☒ This application has been examined ☒ Responsive to communication filed on 12/5/94 ☐ This action is made final.

A shortened statutory period for response to this action is set to expire 3 month(s), 0 days from the date of this letter.
Failure to respond within the period for response will cause the application to become abandoned. 35 U.S.C. 133

Part I THE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:

- ☒ Notice of References Cited by Examiner, PTO-892.
- ☐ Notice of Draftsman's Patent Drawing Review, PTO-948.
- ☐ Notice of Art Cited by Applicant, PTO-1449.
- ☐ Notice of Informal Patent Application, PTO-152.
- ☐ Information on How to Effect Drawing Changes, PTO-1474.
- ☐

Part II SUMMARY OF ACTION

1. ☒ Claims 1-8 and 21-23 are pending in the application.

Of the above, claims 6-8 are withdrawn from consideration.

2. ☐ Claims have been cancelled.

3. ☐ Claims are allowed.

4. ☒ Claims 1-3, 5 and 21-23 are rejected.

5. ☒ Claims 4 are objected to.

6. ☐ Claims are subject to restriction or election requirement.

7. ☐ This application has been filed with informal drawings under 37 C.F.R. 1.85 which are acceptable for examination purposes.

8. ☐ Formal drawings are required in response to this Office action.

9. ☐ The corrected or substitute drawings have been received on Under 37 C.F.R. 1.84 these drawings are ☐ acceptable; ☐ not acceptable (see explanation or Notice of Draftsman's Patent Drawing Review, PTO-948).

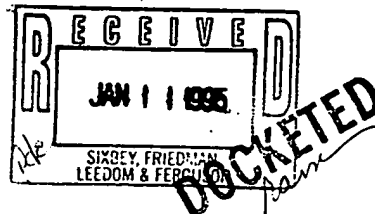
10. ☐ The proposed additional or substitute sheet(s) of drawings, filed on has (have) been ☐ approved by the examiner; ☐ disapproved by the examiner (see explanation).

11. ☐ The proposed drawing correction, filed has been ☐ approved; ☐ disapproved (see explanation).

12. ☒ Acknowledgement is made of the claim for priority under 35 U.S.C. 119. The certified copy has ☒ been received ☐ not been received ☐ been filed in parent application, serial no. ; filed on

13. ☐ Since this application appears to be in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11; 453 O.G. 213.

14. ☐ Other



EXAMINER'S ACTION

Art Unit 2515

Claims 6-8 are withdrawn from further consideration by the examiner, 37 C.F.R. § 1.142(b) as being drawn to a nonelected species. Election was made without traverse in Paper No. 5.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --
(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 and 21 are rejected under 35 U.S.C. § 102(b) as being anticipated by Morozumi '395.

Note in figs.6 and 7 the pixel electrode (41) provided on the substrate (43) and superposed on the gate line with an insulator (47) there between and superposed on the wiring (37) with an insulator (not shown in the figures) therebetween to form a charge holding capacitor (col.6, lines 37-39).

Claim 5 is rejected under 35 U.S.C. § 102(b) as being anticipated by Yokono et al '737.

Note in fig.3A the pixel electrode (G_{n+1}) of n-th row (G_n) and m-th column (S_n) being provided on an opposite side of the data line (S_n) to the pixel electrode (G) of $(n+1)$ th row (G_{m+1}) and m-th column (S_n) . Also, see col.3, lines 33-48.

Claims 1-3 and 22 are rejected under 35 U.S.C. § 102(e) as being anticipated by Iwai et al '017.

Note in figs.2 and 3 the first pixel electrode (10b, left

side) being superposed on the first gate line (lower gate line 1) with an insulator (13) therebetween, and being superposed on the second gate line (upper gate line 1) with another insulator (13) therebetween. Also, note that the portion of the gate line 1 between the data line and the pixel portion (10b) also functions as a gate electrode. Also, note the second pixel electrode (upper left portion of fig.2) being connected with the second gate line (upper gate line 1) through at least a second transistor (upper left portion of fig.2).

Claim 23 is rejected under 35 U.S.C. § 102(b) as being anticipated by JP NO. 60-54478.

Note in fig.3 the pixel electrode (25_2 which also functions as the drain) superposed on the gate line (22_1) with a first insulator (27) therebetween which comprises an anodic oxide of a material of the gate line, and superposed on the wiring (22_2) with a second insulator therebetween (27) which comprises an anodic oxide of a material of the wiring.

Claim 4 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Serial No. 051,313

-4-

Art Unit 2515

Any inquiry concerning this communication should be directed to Tai Duong at telephone number (703) 308-4873.

Duong/ab
December 16, 1994

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SUPERVISORY PATENT EXAMINER
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